Here are the flow control instructions and their Op codes. Please design the programmable logic array below to decode the instructions. Place a fat dot (representing a connection) to put the appropriate input signal on the AND gate. The first two are done for you. They represent the first two 0 digits of the 6-bit j op code.

|  |  |
| --- | --- |
| Instruction | Op5-Op0 |
| j | 0x02 |
| jal | 0x03 |
| beq | 0x04 |
| Bne | 0x05 |

